INDEX
VOLUME 3: 1988

AUTHOR

Bedi, S File system activity and the distribution of disc transfers on a multiaccess system 199

Datta, A K, Ghosh, S, Gopalan, M and Elmargarmid, A K Pipelining and concurrency in graphics and image-processing applications 107

Datta, A K, Ghosh, S, Harms, D and Elmargarmid, A K Deadlock-avoidance mechanisms in distributed systems 67

Giandomenico, F D, Guidotti, M L, Grandoni, F and Simocini, L Gracefully degradable algorithm for Byzantine agreement 32

Guha, R K, Dickinson, A F and Kikawa, A An analysis of CMH deadlock-detection algorithm using transmission delays 181

Harrison, E S and Schmitt, E J The structure of System/88, a fault-tolerant computer 140

Hughes, H, Yegani, P and Yih, J-S CSMA/CD protocol with message based priority function 13

Iyer, B R, Dias, D M and Yu, P S Performability comparison of configurable duplex systems 201

Mancini, L and Pappalardo, G Hoare logic of distributed redundant systems 171

Molnar, R K and Bruell, S C Sensitivity of operational formulae 51

Pakzad, S and Lakshmivarahan, S Interconnection networks and fault tolerance 91

Reddi, A V Secondary memory analysis of a mini/microcomputer in resource-sharing distributed information systems 189

Rego, V and Szpankowski, W Closed-network duals of multiqueues with application to token-passing systems 127

Robach, C and Guibert, S Testability measures: a review 117

Schröder, H Instruction systolic array—tradeoff between flexibility and speed 83

Shatz, S M Superprocesses: a distributed program configuration concept supporting hardware fault tolerance 3

Shin, K G and Lee, H Port manipulator for the distributed realization of an integrated manufacturing system 21

TITLE

An analysis of CMH deadlock-detection algorithm using transmission delays **Guha, R K, Dickinson, A F and Kikawa, A** 181

Closed-network duals of multiqueues with application to token-passing systems **Rego, V and Szpankowski, W** 127

CSMA/CD protocol with message-based priority function **Hughes, H, Yegani, P and Yih, J-S** 13

Deadlock-avoidance mechanisms in distributed systems **Datta, A K, Ghosh, S, Harms, D and Elmagarmid, A K** 67

File system activity and the distribution of disc transfers on a multiaccess system **Bedi, S** 199

Gracefully degradable algorithm for Byzantine agreement **Giandomenico, F D, Guidotti, M L, Grandoni, F and Simoncini, L** 32

Hoare logic of distributed redundant systems **Mancini, L and Pappalardo, G** 171

Instruction systolic array—tradeoff between flexibility and speed **Schröder, H** 83

Interconnection networks and fault tolerance **Pakzad, S and Lakshmivarahan, S** 91

Performability comparison of configurable duplex systems **Iyer, B R, Dias, D M and Yu, P S** 201

Pipelining and concurrency in graphics and image-processing applications **Datta, A K, Ghosh, S, Gopalan, M and Elmargarmid, A K** 107

Port manipulator for the distributed realization of an integrated manufacturing system **Shin, K G and Lee, H** 21

Secondary memory analysis of a mini/microcomputer in resource-sharing distributed information systems **Reddi, A V** 189

Sensitivity of operational formulae **Molnar, R K and Bruell, S C** 51

Superprocesses: a distributed program configuration concept supporting hardware fault tolerance **Shatz, S M** 3

Testability measures: a review **Robach, C and Guibert, S** 117

The structure of System/88, a fault-tolerant computer **Harrison, E S and Schmitt, E J** 140

SUBJECT

Book reviews

Fault-tolerant and fault-testable hardware design 222

Frontiers of supercomputing 45

Good programming practice in Ada 221

Microprocessors for engineers: interfacing for real time applications 220

Nested transactions: an approach to reliabile distributed computing 219

Parallel programming in ANSI standard Ada 218

Reduced instruction set computer architectures for VLSI 223

Resilient computing systems 220

Scientific Ada 218

Software reliability—achievement and assessment 221

Synchronization of parallel algorithms 222

The connection machine 45

Calendar 47, 104, 168, 232

Conferences

International workshop on Petri nets and performance modelling 217

News 41, 101, 163, 224

Publications

Networks and distributed computation 231

Parallel processing 231

Transputer Reference manual 231

INDEX
VOLUME 3: 1988

AUTHOR

Bedi, S File system activity and the distribution of disc transfers on a multiaccess system 199

Datta, A K, Ghosh, S, Gopalan, M and Elmargarmid, A K Pipelining and concurrency in graphics and image-processing applications 107

Datta, A K, Ghosh, S, Harms, D and Elmargarmid, A K Deadlock-avoidance mechanisms in distributed systems 67

Giandomenico, F D, Guidotti, M L, Grandoni, F and Simocini, L Gracefully degradable algorithm for Byzantine agreement 32

Guha, R K, Dickinson, A F and Kikawa, A An analysis of CMH deadlock-detection algorithm using transmission delays 181

Harrison, E S and Schmitt, E J The structure of System/88, a fault-tolerant computer 140

Hughes, H, Yegani, P and Yih, J-S CSMA/CD protocol with message based priority function 13

Iyer, B R, Dias, D M and Yu, P S Performability comparison of configurable duplex systems 201

Mancini, L and Pappalardo, G Hoare logic of distributed redundant systems 171

Molnar, R K and Bruell, S C Sensitivity of operational formulae 51

Pakzad, S and Lakshmivarahan, S Interconnection networks and fault tolerance 91

Reddi, A V Secondary memory analysis of a mini/microcomputer in resource-sharing distributed information systems 189

Rego, V and Szpankowski, W Closed-network duals of multiqueues with application to token-passing systems 127

Robach, C and Guibert, S Testability measures: a review 117

Schröder, H Instruction systolic array—tradeoff between flexibility and speed 83

Shatz, S M Superprocesses: a distributed program configuration concept supporting hardware fault tolerance 3

Shin, K G and Lee, H Port manipulator for the distributed realization of an integrated manufacturing system 21

TITLE

An analysis of CMH deadlock-detection algorithm using transmission delays **Guha, R K, Dickinson, A F and Kikawa, A** 181

Closed-network duals of multiqueues with application to token-passing systems **Rego, V and Szpankowski, W** 127

CSMA/CD protocol with message-based priority function **Hughes, H, Yegani, P and Yih, J-S** 13

Deadlock-avoidance mechanisms in distributed systems **Datta, A K, Ghosh, S, Harms, D and Elmagarmid, A K** 67

File system activity and the distribution of disc transfers on a multiaccess system **Bedi, S** 199

Gracefully degradable algorithm for Byzantine agreement **Giandomenico, F D, Guidotti, M L, Grandoni, F and Simoncini, L** 32

Hoare logic of distributed redundant systems **Mancini, L and Pappalardo, G** 171

Instruction systolic array—tradeoff between flexibility and speed **Schröder, H** 83

Interconnection networks and fault tolerance **Pakzad, S and Lakshmivarahan, S** 91

Performability comparison of configurable duplex systems **Iyer, B R, Dias, D M and Yu, P S** 201

Pipelining and concurrency in graphics and image-processing applications **Datta, A K, Ghosh, S, Gopalan, M and Elmargarmid, A K** 107

Port manipulator for the distributed realization of an integrated manufacturing system **Shin, K G and Lee, H** 21

Secondary memory analysis of a mini/microcomputer in resource-sharing distributed information systems **Reddi, A V** 189

Sensitivity of operational formulae **Molnar, R K and Bruell, S C** 51

Superprocesses: a distributed program configuration concept supporting hardware fault tolerance **Shatz, S M** 3

Testability measures: a review **Robach, C and Guibert, S** 117

The structure of System/88, a fault-tolerant computer **Harrison, E S and Schmitt, E J** 140

SUBJECT

Book reviews

Fault-tolerant and fault-testable hardware design 222

Frontiers of supercomputing 45

Good programming practice in Ada 221

Microprocessors for engineers: interfacing for real time applications 220

Nested transactions: an approach to reliabile distributed computing 219

Parallel programming in ANSI standard Ada 218

Reduced instruction set computer architectures for VLSI 223

Resilient computing systems 220

Scientific Ada 218

Software reliability—achievement and assessment 221

Synchronization of parallel algorithms 222

The connection machine 45

Calendar 47, 104, 168, 232

Conferences

International workshop on Petri nets and performance modelling 217

News 41, 101, 163, 224

Publications

Networks and distributed computation 231

Parallel processing 231

Transputer Reference manual 231